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IN THE CLAIMS

Please amend the claims as follows. This claim set is to replace all prior versions.

1. (original) An integrated circuit device comprising:
 - a substrate;
 - a first conductive electrode on the substrate, the first conductive electrode having an electrode wall extending away from the substrate;
 - an insulating spacer on the electrode wall wherein portions of the electrode wall are free of the insulating spacer between the substrate and the insulating spacer;
 - a capacitor dielectric layer on portions of the first conductive electrode free of the spacer;
 - and
 - a second conductive electrode on the capacitor dielectric layer opposite the first conductive electrode, wherein a thickness of the insulating spacer between the first and second conductive electrodes is greater than a thickness of the capacitor dielectric layer between the first and second conductive electrodes.
2. (original) An integrated circuit device according to Claim 1 wherein the capacitor dielectric layer and the insulating spacer comprise different materials.
3. (original) An integrated circuit device according to Claim 1 further comprising:
 - a transistor on the substrate;
 - an insulating layer between the substrate and the first conductive electrode; and
 - a conductive plug providing electrical coupling between the first conductive electrode and a source/drain region of the transistor through the insulating layer.
4. (original) An integrated circuit device according to Claim 1 wherein portions of the electrode wall extend beyond the spacer away from the substrate free of the insulating spacer, and wherein the capacitor dielectric layer is also on portions of the electrode wall extending beyond the spacer.

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5. (original) An integrated circuit device according to Claim 1 wherein the electrode wall includes a recessed portion and wherein the insulating spacer is on the recessed portion of the electrode wall.

Claims 6-10 (canceled).

11. (original) An electronic device comprising:

a substrate;

a conductive electrode on the substrate, the conductive electrode having an electrode wall extending away from the substrate; and

an insulating spacer on the electrode wall wherein portions of the electrode wall are free of the insulating spacer between the substrate and the insulating spacer.

12. (original) An electronic device according to Claim 11 wherein portions of the electrode wall extend from the insulating spacer away from the substrate free of the insulating spacer.

13. (original) An electronic device according to Claim 11 wherein the electrode wall includes a recessed portion and wherein the insulating spacer is on the recessed portion of the electrode wall.

14. (original) An electronic device according to Claim 11 wherein the electrode wall is closed thereby defining an inside of the electrode wall and an outside of the electrode wall.

15. (original) An electronic device according to Claim 14 wherein the electrode wall defines a cylinder.

16. (original) An electronic device according to Claim 11 further comprising:

a capacitor dielectric layer on portions of the conductive electrode free of the spacer.

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17. (original) An electronic device according to Claim 16 further comprising:
a second conductive electrode on the capacitor dielectric layer opposite the first electrode.

18. (original) An electronic device according to Claim 17 wherein the spacer has a first thickness separating the conductive electrodes, wherein the capacitor dielectric layer has a second thickness separating the conductive electrodes, and wherein the first thickness is greater than the second thickness.

19. (original) An electronic device according to Claim 11 wherein the substrate includes a transistor and wherein the conductive electrode is electrically coupled with a source/drain region of the transistor.

20. (original) An electronic device according to Claim 11 further comprising:
a sacrificial layer on the substrate, wherein the sacrificial layer has a thickness on the substrate such that the sacrificial layer extends to the insulating spacer and wherein the sacrificial layer and the insulating spacer comprise different materials.

Claims 21-39 (canceled).

40. (original) An electronic device comprising:
a substrate; and
a conductive electrode on the substrate, the conductive electrode having an electrode wall extending away from the substrate, the electrode wall including a recessed portion at an end thereof opposite the substrate.

41. (original) An electronic device according to Claim 40 further comprising:

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an insulating spacer on the recessed portion of the electrode wall wherein portions of the electrode wall are free of the insulating spacer between the substrate and the insulating spacer.

42. (original) An electronic device according to Claim 41 wherein portions of the electrode wall extend from the insulating spacer away from the substrate free of the insulating spacer.

43. (original) An electronic device according to Claim 40 wherein the electrode wall is closed thereby defining an inside of the electrode wall and an outside of the electrode wall.

44. (original) An electronic device according to Claim 43 wherein the electrode wall defines a cylinder.

45. (original) An electronic device according to Claim 40 further comprising:
a capacitor dielectric layer on portions of the conductive electrode.

46. (original) An electronic device according to Claim 45 further comprising:
a second conductive electrode on the capacitor dielectric layer opposite the first electrode.

47. (original) An electronic device according to Claim 46 further comprising:
an insulating spacer on the recessed portion of the electrode wall wherein portions of the electrode wall are free of the insulating spacer between the substrate and the insulating spacer wherein the spacer has a first thickness separating the conductive electrodes, wherein the capacitor dielectric layer has a second thickness separating the conductive electrodes, and wherein the first thickness is greater than the second thickness.

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48. (original) An electronic device according to Claim 40 wherein the substrate includes a transistor and wherein the conductive electrode is electrically coupled with a source/drain region of the transistor.

49. (original) An electronic device according to Claim 40 further comprising:
a sacrificial layer on the substrate, wherein the sacrificial layer has a thickness on the substrate such that the sacrificial layer extends to the recessed portion of the electrode wall and wherein the recessed portion of the electrode wall is free of the sacrificial layer.

Claims 50-67 (canceled).